



Cambricon

中科赛武纪



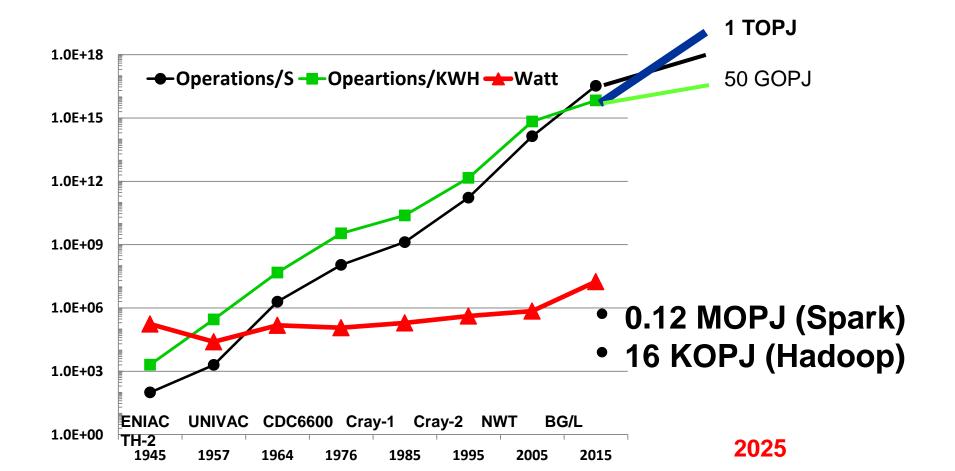
Zhiwei Xu Institute of Computing Technology (ICT) Chinese Academy of Sciences (CAS) http://novel.ict.ac.cn/zxu/ zxu@ict.ac.cn

Research supported in part by the CAS Strategic Priority Program (XDB02040009), the NSF of China (61532016), and the MOST (2016YFB1000200).

Trends of ICT Developed Computers Speed, software complexity, power Exaflops (10¹⁸) Z Xu, G Li, Computing for the Masses, **Datacenter for** Communications of ACM, 54(10): 129-137 (2011) 100's M (10⁸) users **10**¹⁶ **10**¹⁵ World Top1 computer speed (Flops) **10**¹⁴ ICT computer speed (Flops) **10**¹³ ICT computer system software (LOC) ICT computer power (W) 10¹² **10**¹¹ **10**¹⁰ Often overlooked! 10⁹ 100 M (10⁸) LOC 10⁸ 100 M (10⁸) W **10**⁷ **10**⁶ Needs: **10**⁵ D5KA Maintain growth in D4KA **10**⁴ KJ8920 performance, but D4KL **10**³ control power & 013 757 Nebulae 119C **10**² D2KB system software 111 104 119B D2KA complexity **10**¹ 119 D1K 103 10⁰ D1 2020-2030 1960 1970 1980 2000 2010 1950 1990

Fundamental Challenge First time in 70 years

Energy efficiency growth lags behind speed growth
Big data computing is especially bad: 15-150 KOPJ



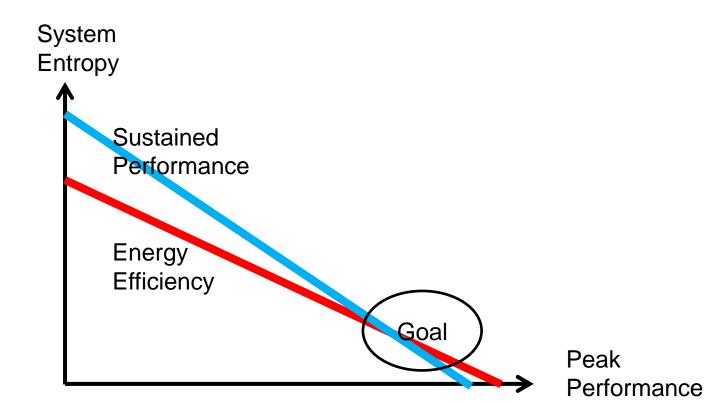
Workload Complexity and Sys Disorder Keep Growing

- Three types of workloads share a Sugon HPC system
 - Traditional HPC (scientific and engineering)
 - Data analytics (online and offline)
 - Machine learning, especially deep learning
- Many types of disorder exist
 - Workload dynamicity and uncertainty
 - Interferences of workloads
 - System jitter (clutter, noise)
 - Impedance mismatch
 - Unbounded flexibility (Gordon Bell: general-purpose always wins)
- Efficiency needs order
 - Uncertainty bounding will be a main, fundamental challenge
 - A key metric: computing systems entropy (cf., tail latency)
 - Low-entropy systems with disciplined flexibility (Symphony vs. Bazaar)

Workloads	
System	I
Technology	

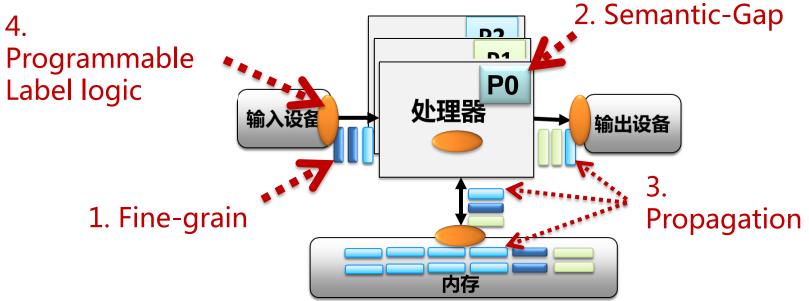
Low Entropy Hypothesis

- As computing system entropy decreases,
 - Energy efficiency increases, and
 - Sustained performance increases



Labeled von Neumann Architecture

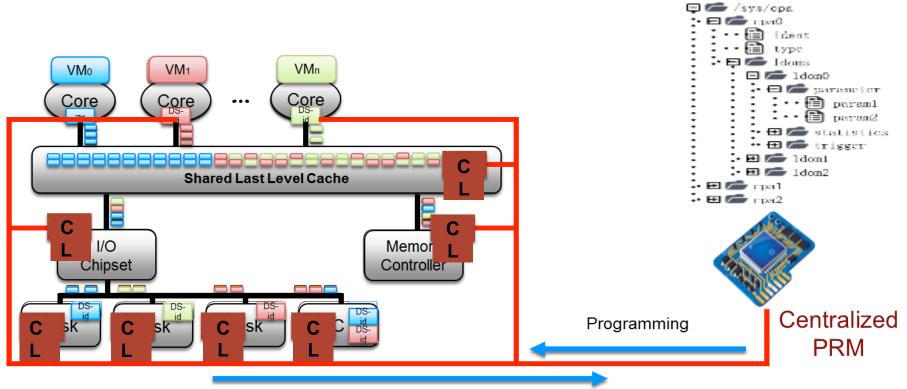
- Fine-grain: attach a label to each memory and I/O request
- **Semantic-Gap**: relate labels to VM/Proc/Thread/Var
- **Propagation**: propagate labels in the whole machine
- Programmable label logic: provide differentiated services based on different label-indexed rules



Bao and Wang, Labeled von Neumann Architecture for Software-Defined Cloud, Journal of Computer Science and Technology, 2017 Vol. 32 (2): 219-223.

Programmable Architecture for Resourcing-on-Demand

Ma et. al, Supporting Differentiated Services in Computers via Programmable Architecture for Resourcing-on-Demand (PARD), *ASPLOS*, 2015

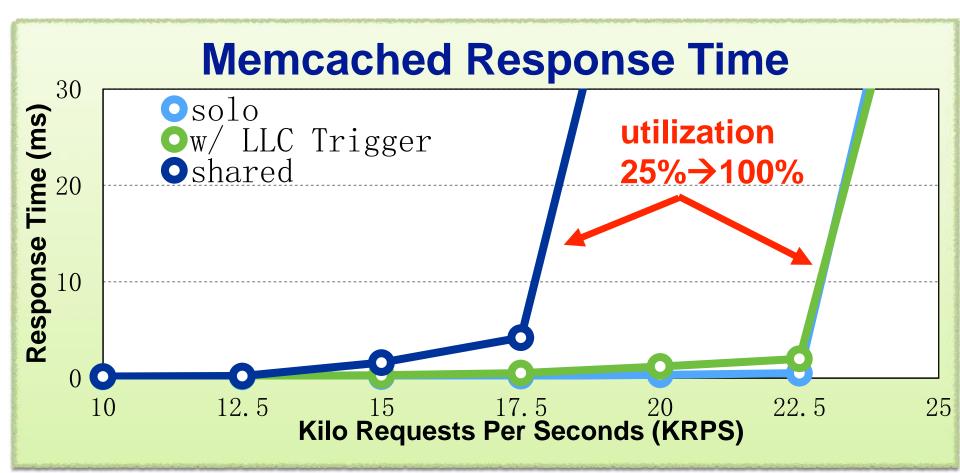


Monitoring & Interrupts

Improve Utilization w/o Loss of QoS

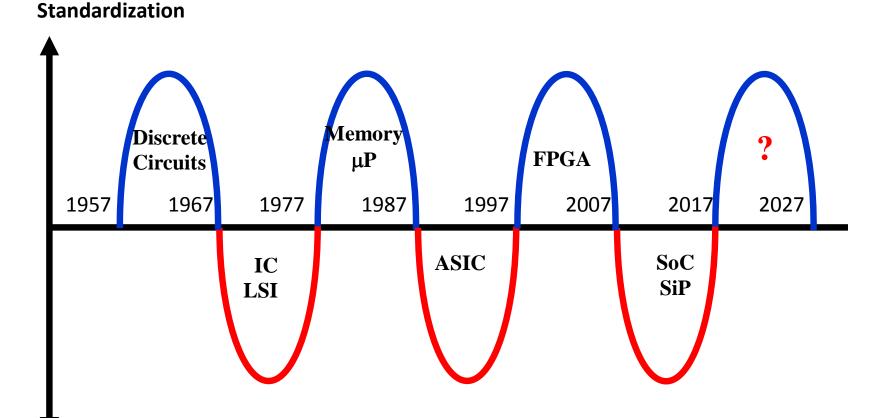
CPU Utilization 4X

• Memcached: Tail Latency <1.5ms</p>



Makimoto's Wave

• Semiconductor technology will soon enter another phase change. But what is it?

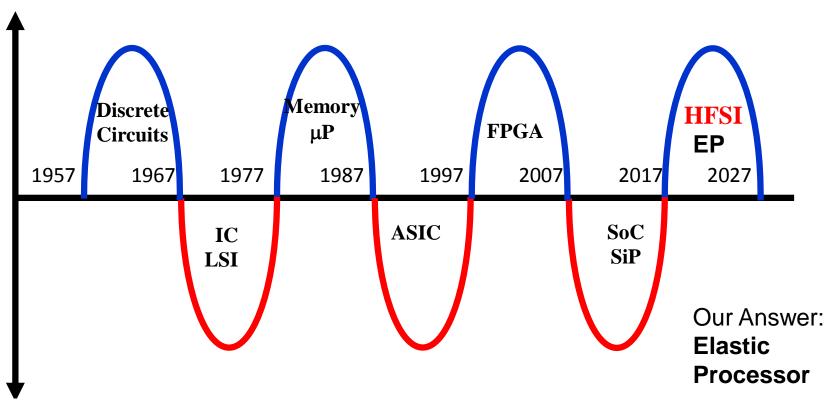


Customization

Tsugio Makimoto, "Implications of Makimoto's Wave", *IEEE Computer*, vol. 46, no. , pp. 32-37, Dec. 2013

Makimoto's Wave

- HFSI: Highly Flexible Super Integration
 - Redundant circuits can be shut off when not in use

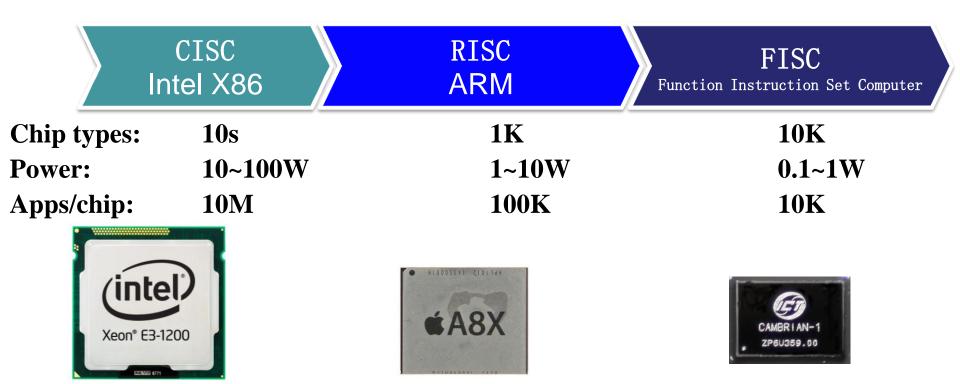


Standardization

Customization

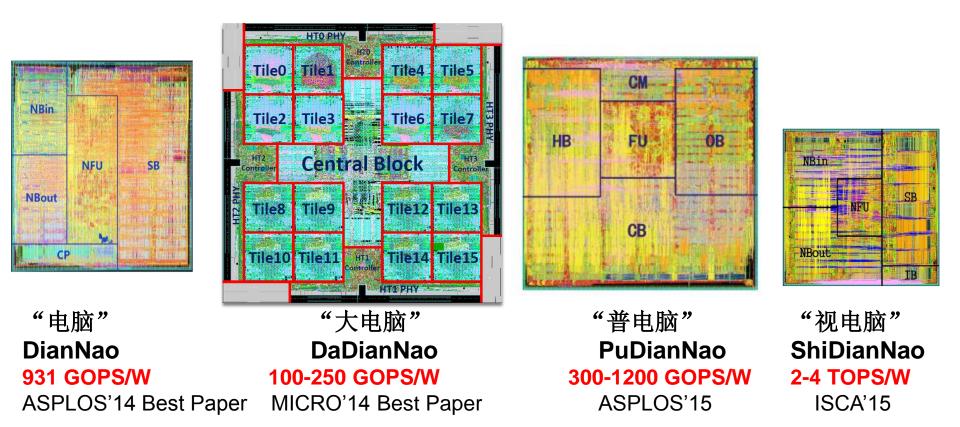
Elastic Processor

- A new architecture style (FISC)
 - Featuring function instructions executed by programmable ASIC accelerators
 - Targeting 1000 GOPS/W = 1 TOPJ



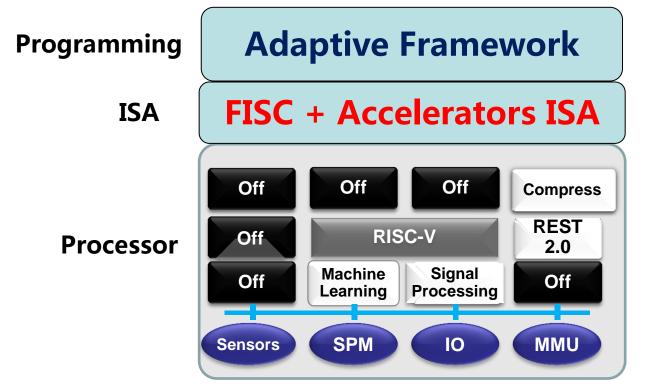
The DianNao Family Showing Potential for TOPJ

Yunji Chen, Tianshi Chen, Zhiwei Xu, Ninghui Sun, Olivier Temam. DianNao family: energy-efficient hardware accelerators for machine learning. *Communications of the ACM* 59(11): 105-112 (2016) Research Highlights paper



Elastic Processor

- Accelerators dominate (>99% of the time)
 - Dynamic customization, low switching overhead
 - Accelerator ISA
- Adaptive runtime with >50% efficiency

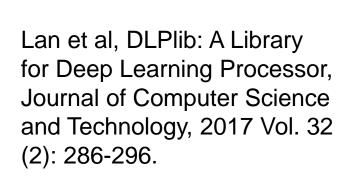


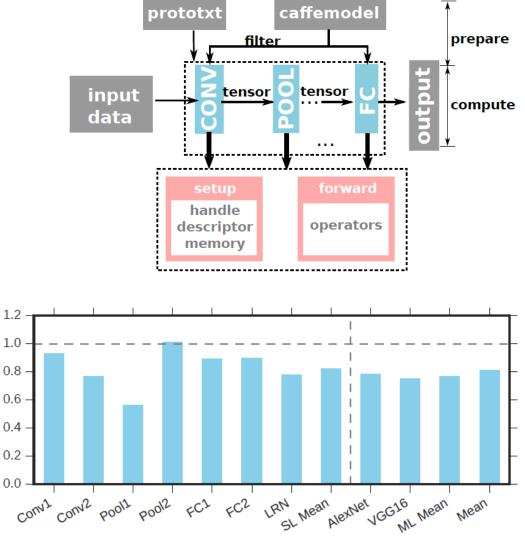
Liu et al, Cambricon: An Instruction Set Architecture for Neural Networks, in *Proceedings of the 43rd ACM/IEEE International Symposium on Computer Architecture (ISCA'16)*, 2016.

Adaptive Framework

Rate

- No pollution to Caffe
- Tensor+Filter
- 79% of assembly codes performance





Benchmarks

谢谢! Thank you!

zxu@ict.ac.cn http://novel.ict.ac.cn/zxu/